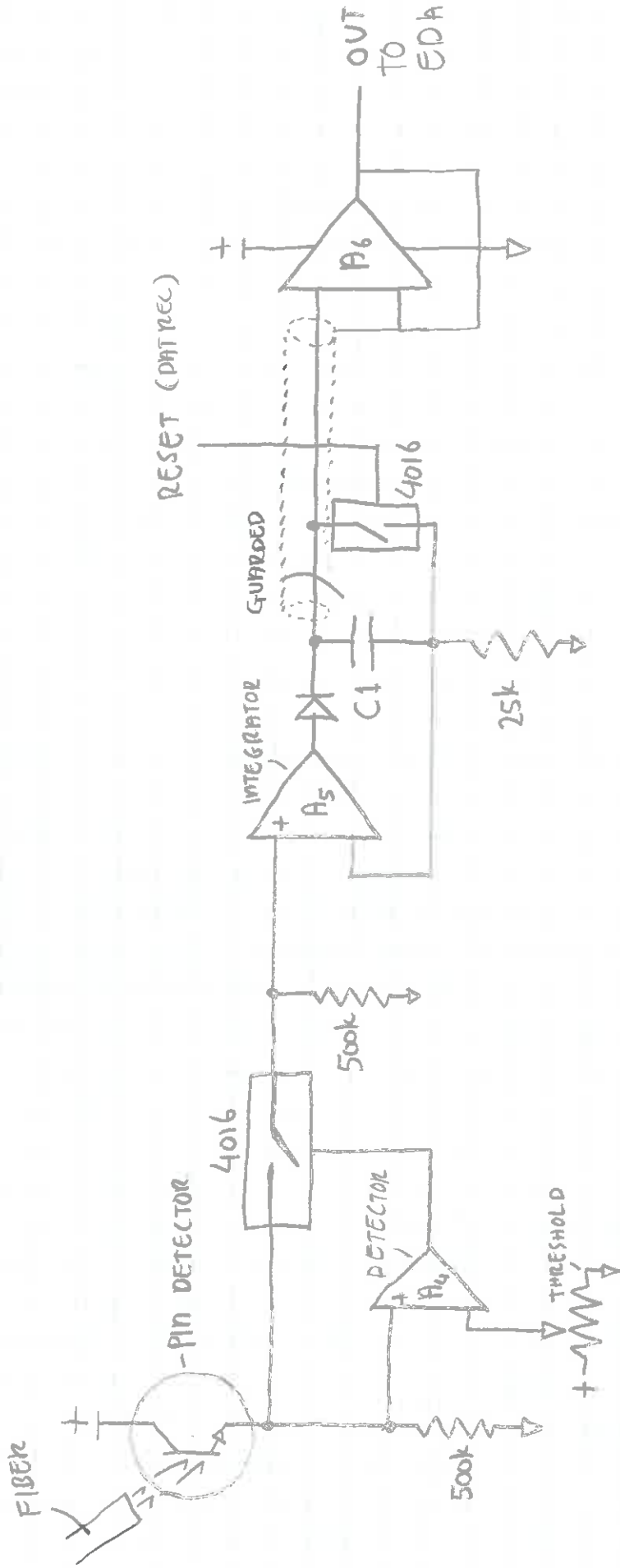


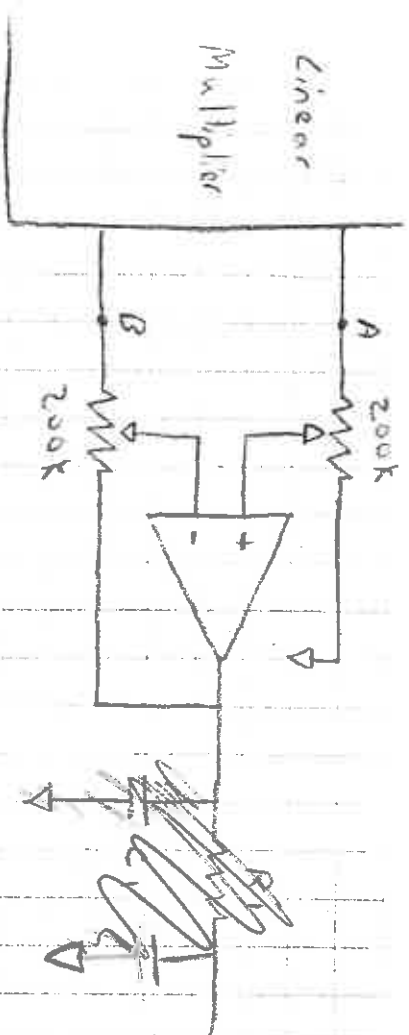
INTEGRATOR



Op-Amps : 357 $V/\mu s$
 SLEW 50 $V/\mu s$
 BW 15 MHz
 JFET INPUT
 OR EQUIVALENT

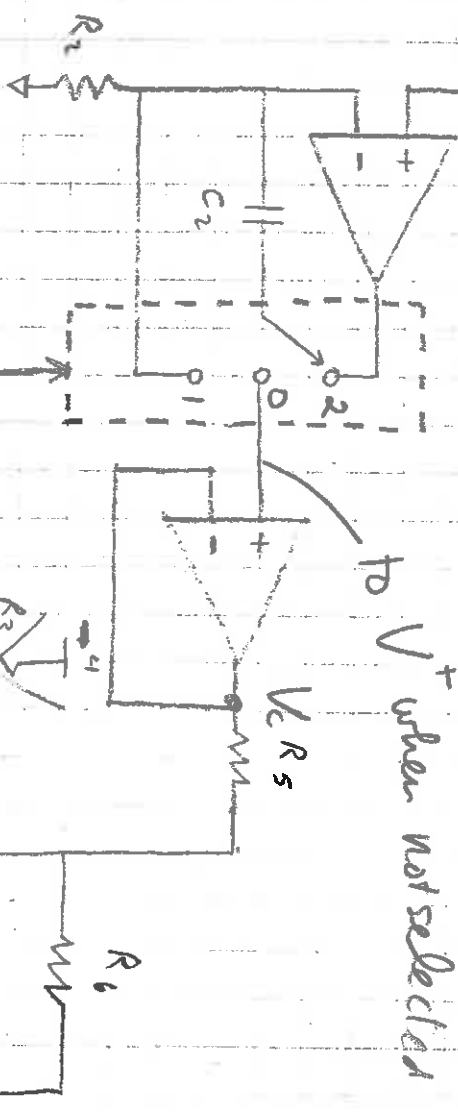
4016 QUAD CMOS
 BILATERAL SWITCH

FORS MAP ED CHEUNG



$V_c = 22 \text{ mV/Vce}$ clinks
 $I_c = 47.8 \text{ nA}$
 error 20mV

$$\text{Error} = \frac{2 \cdot 10^{-3}}{5} = 0.4\%$$

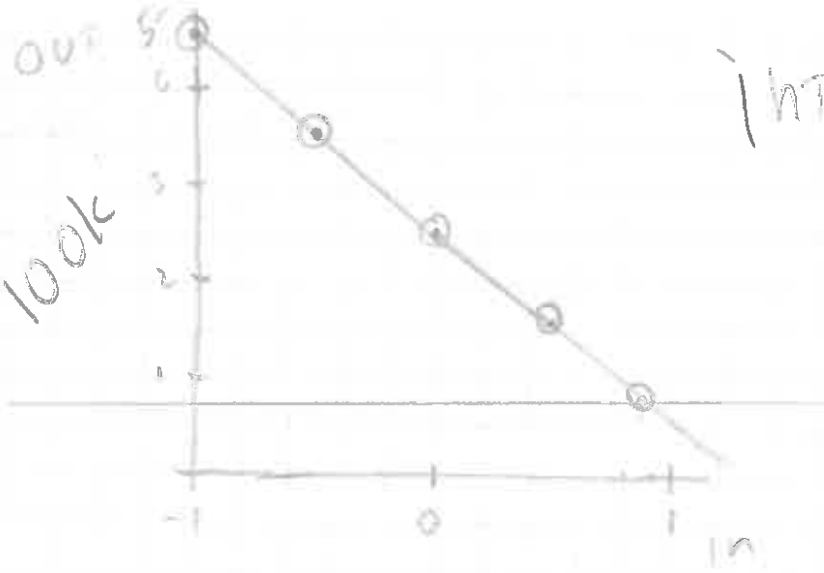


to V^+ when not selected

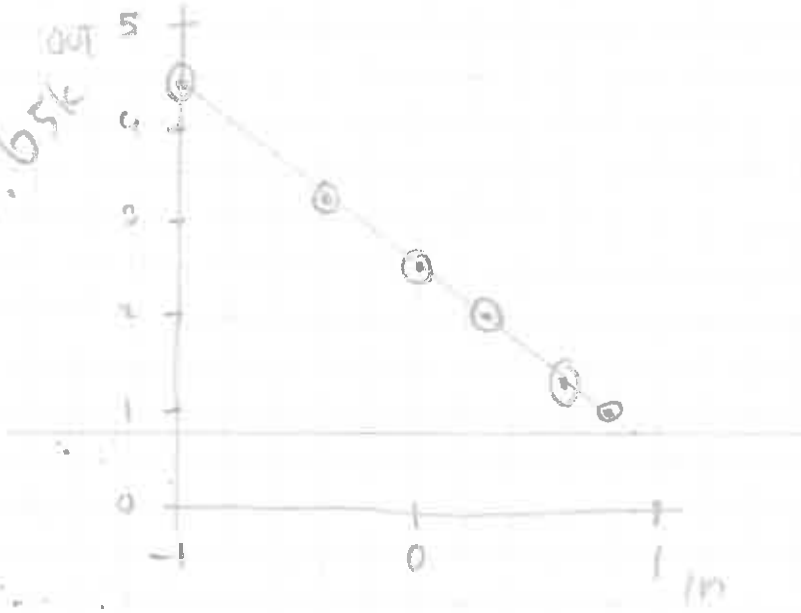
INT	RST	#	action
0	0	0	wait
0	1	1	reset
1	0	2	integrate

INTEGRATOR only

$R = 100k$

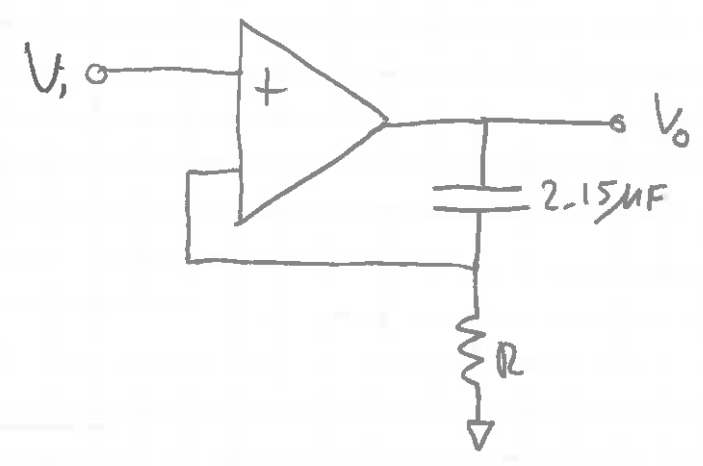
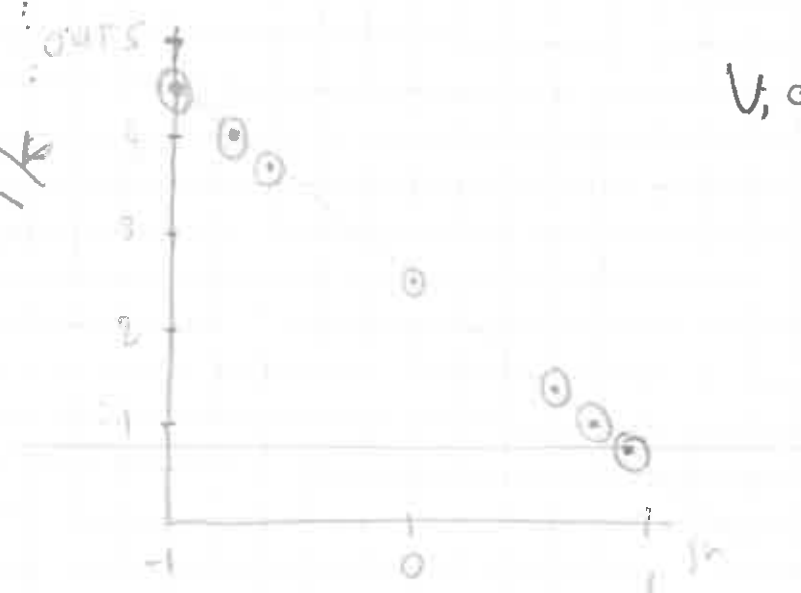


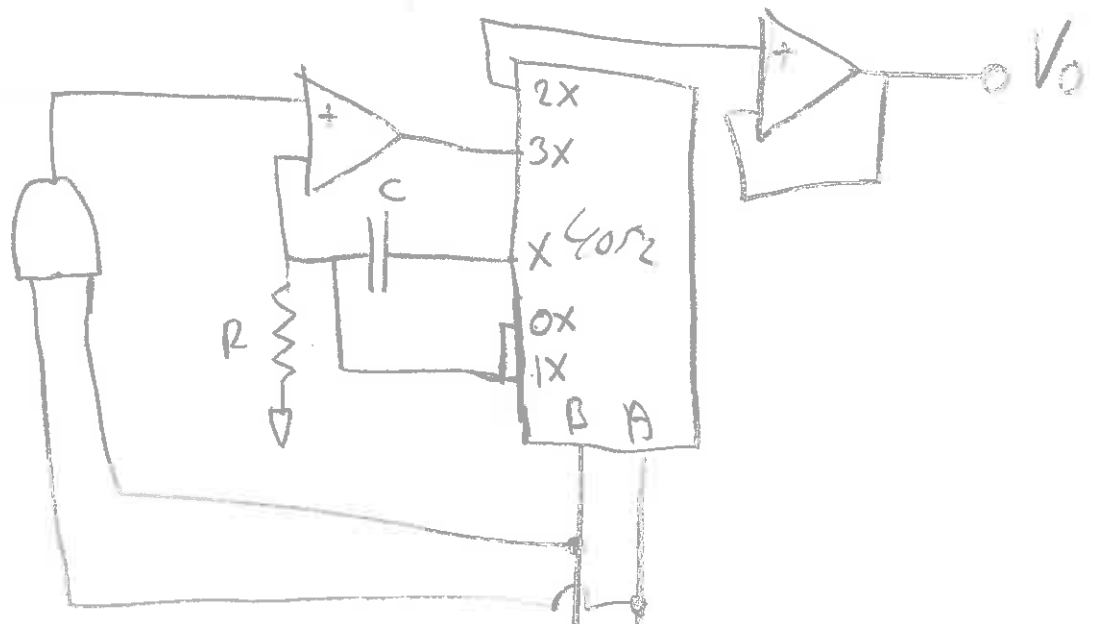
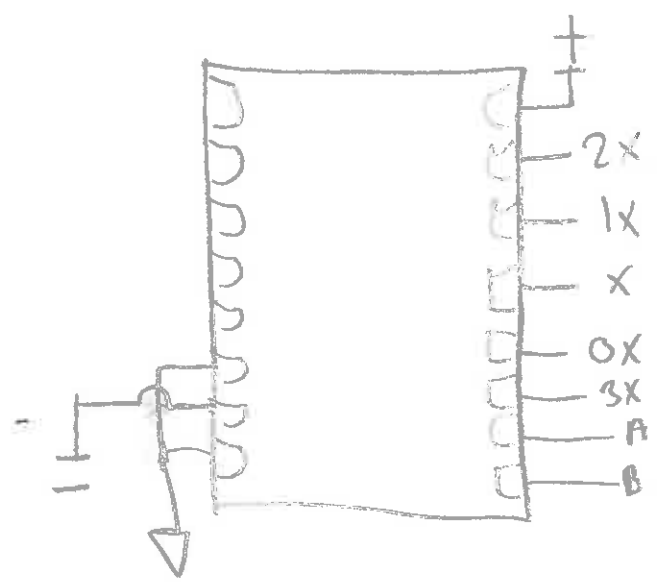
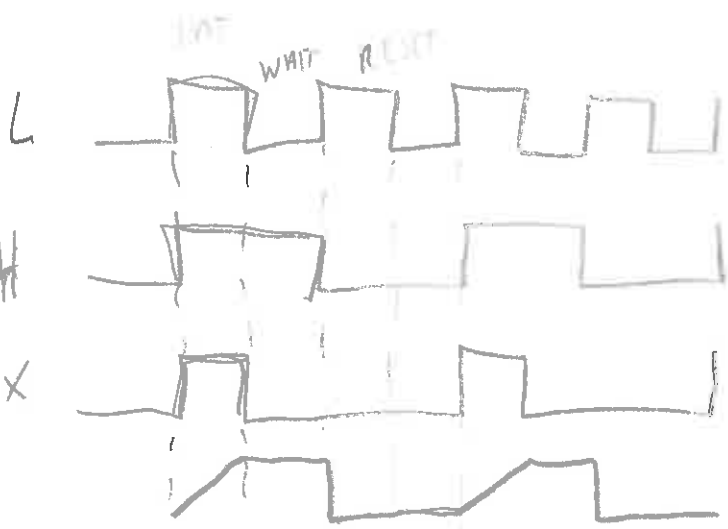
$10.65k$



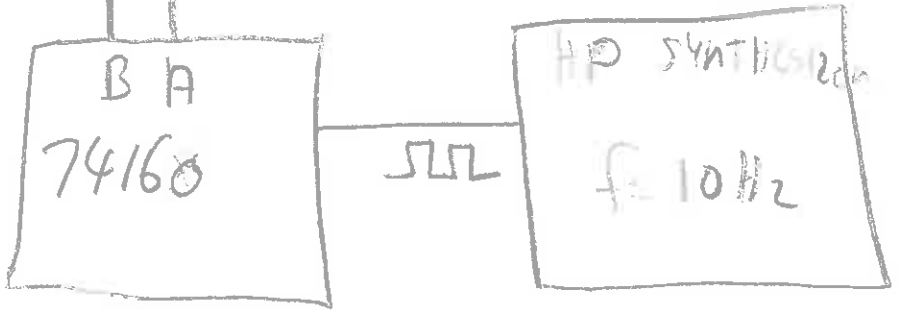
~~miss on 5V-E
early 15.85~~

$111k$





TEST
 V_{CC}
 MAX OUTPUT
 WITH CLIPPING

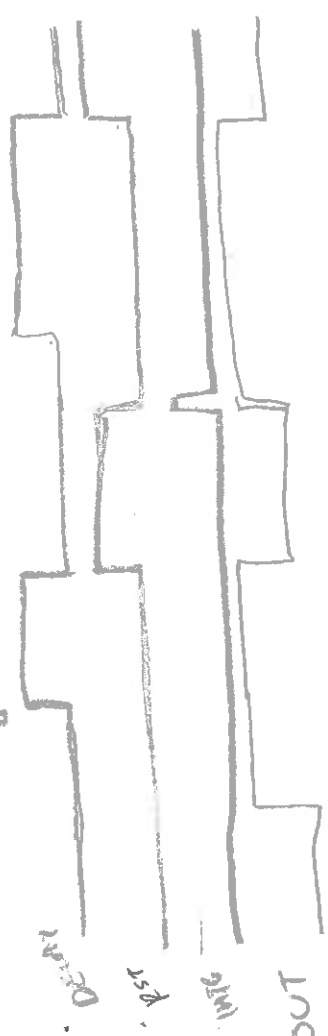


INTEGRATOR DETAILED

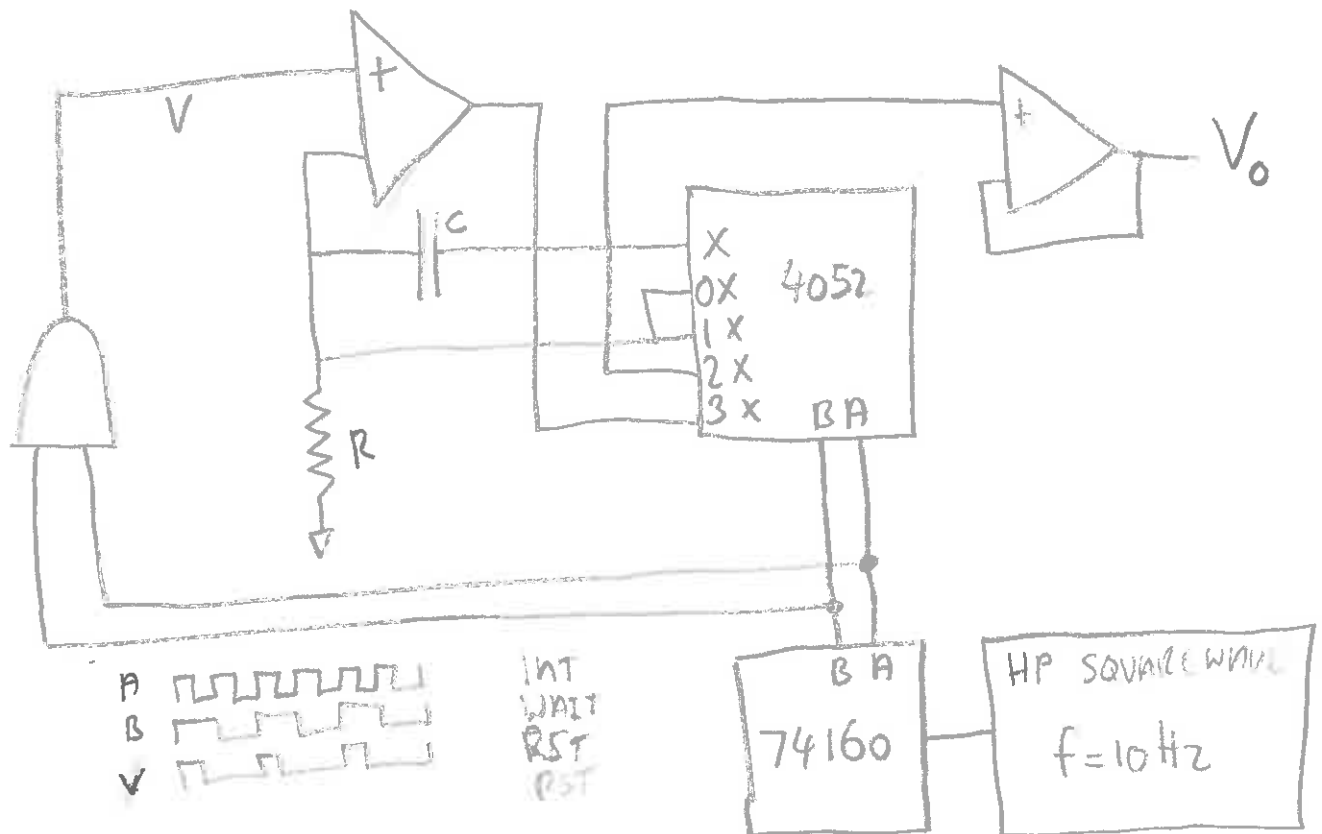
Op amps : *24
(324)



INTG	RST	FUNCTION
0	0	WAIT
1	0	INTEGRATE
0	1	RESET
1	1	no GO



INTEGRATOR TEST CIRCUIT



$$I = \frac{V}{R}$$

$$V_{out} = \frac{Vt}{RC}$$

$$t = 50 \text{ msec}$$

$$V_{out} = \frac{I \cdot t}{C}$$

assume $V_{out} = V$

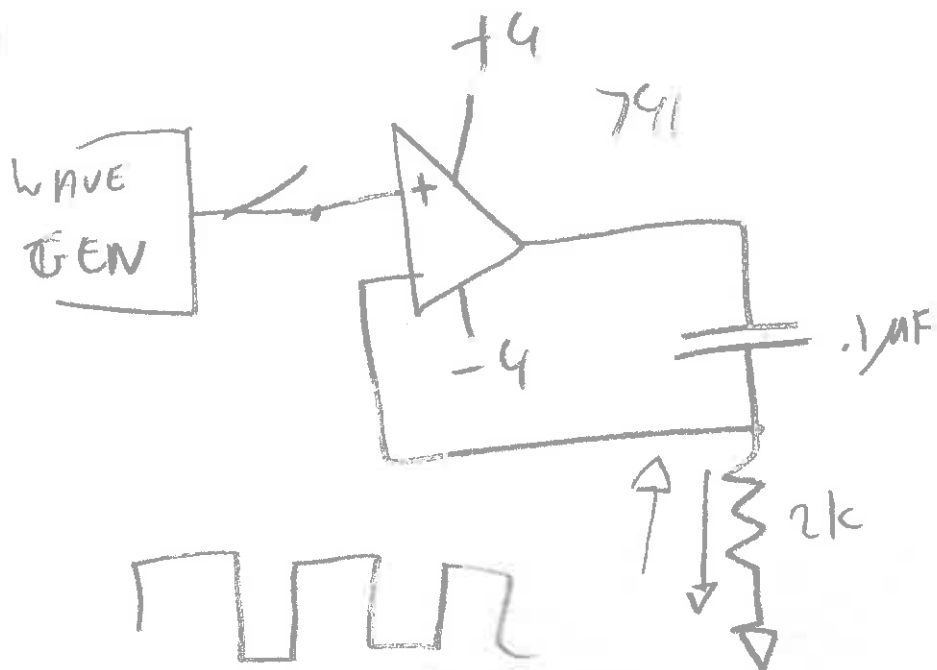
$$RC = 50 \cdot 10^{-3}$$

$$C = 2.15 \mu\text{F}$$

$$R = 23,255 \Omega$$

$$R = 25 \text{ k}\Omega$$

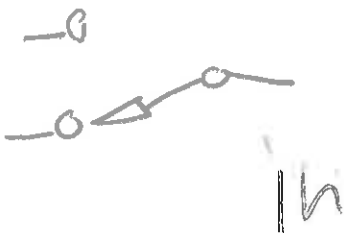
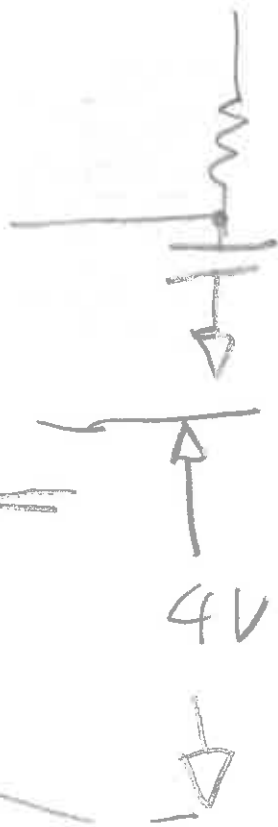
WORKING INTEGRATOR



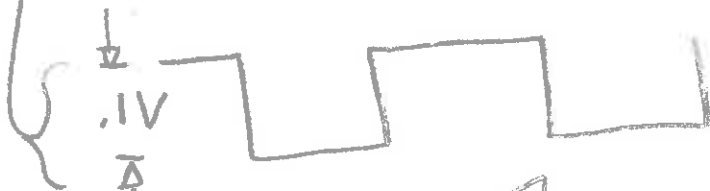
non linear at
217 Hz



OUT

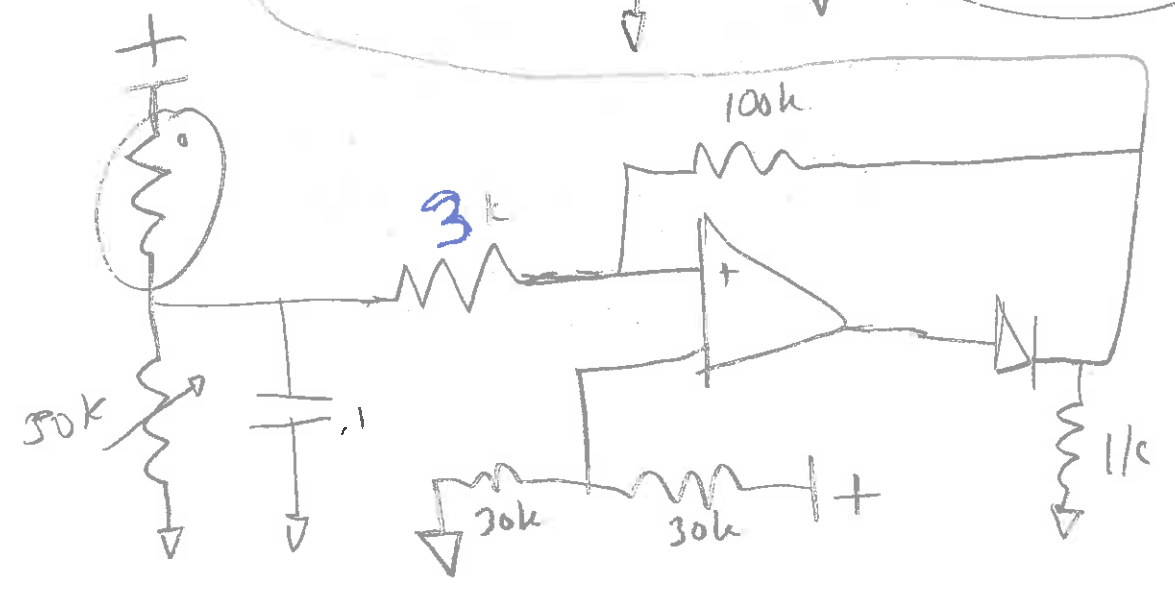
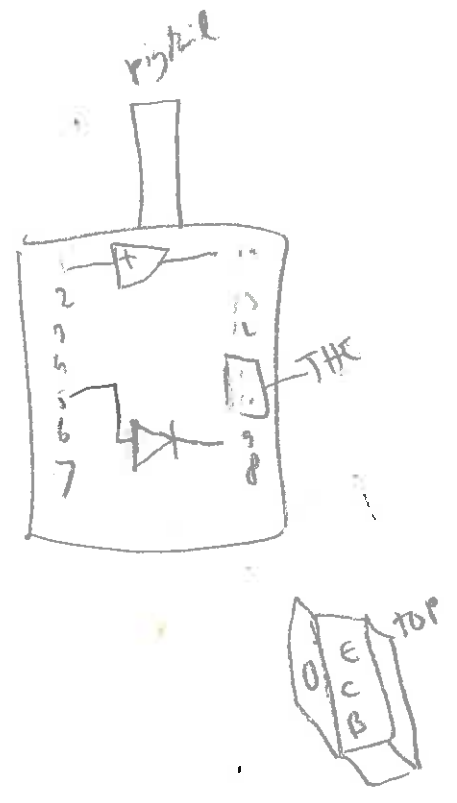
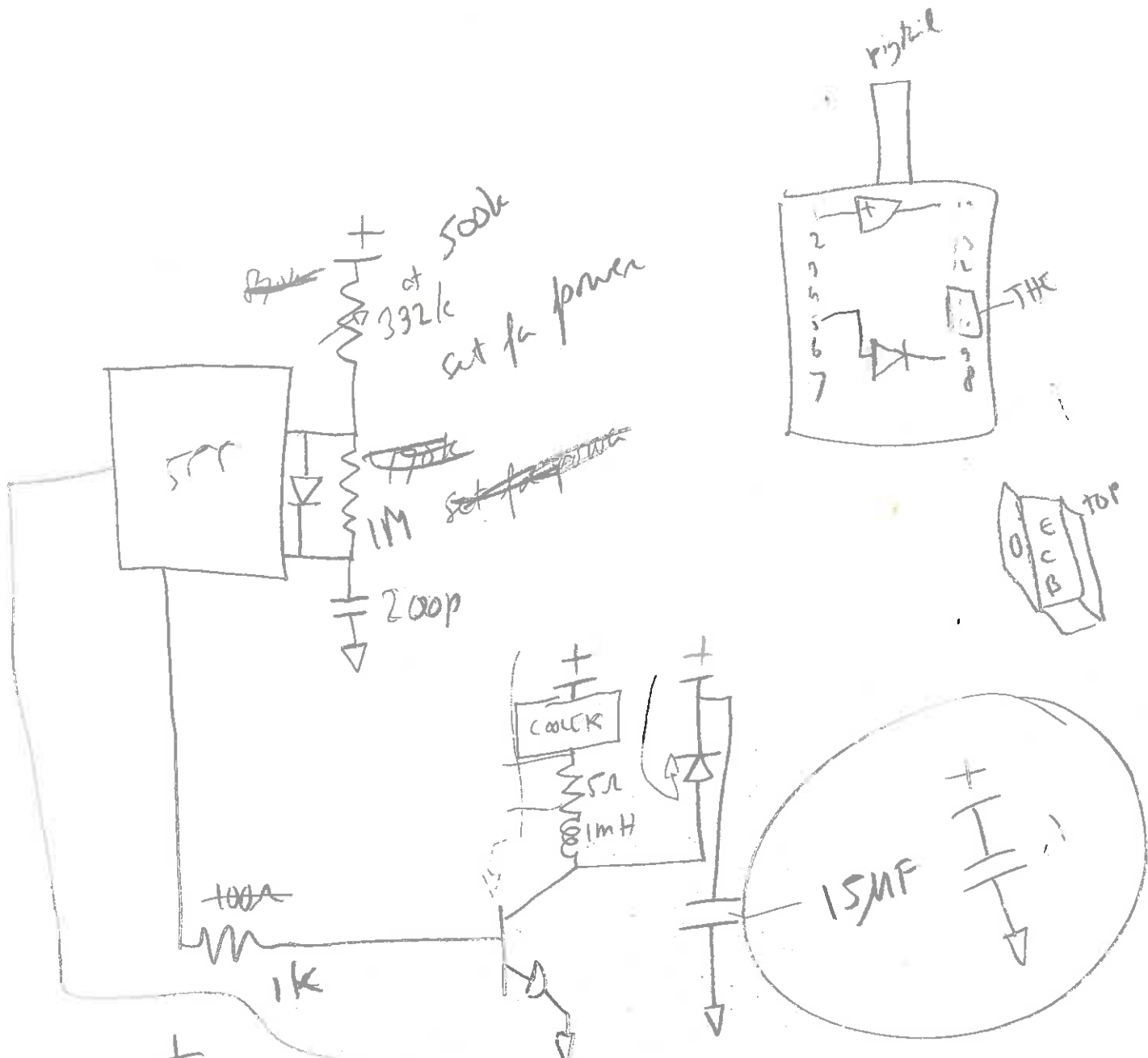


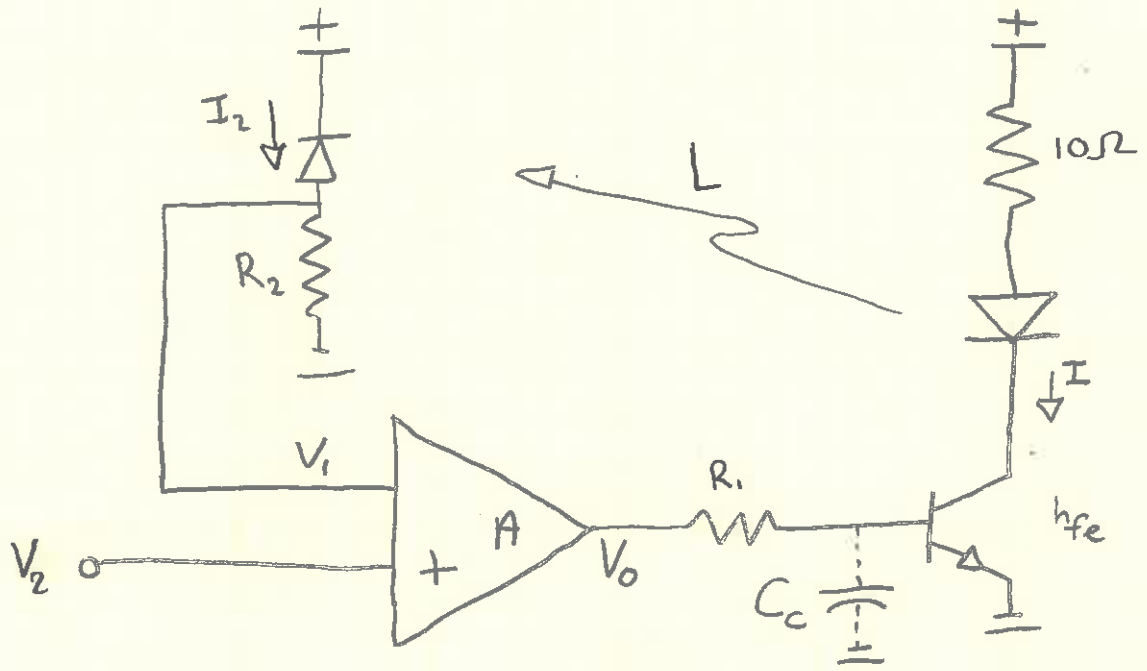
-6mV offset
(Bias currents)



f = 20 Hz







$$V_0 = (V_2 - V_1) A$$

$$I = h_{fe} \left(\frac{V_0 - 0.6}{R_1} \right)$$

$$L = D[I]$$

$$V_1 = I_2 \cdot R_2$$

$$I_2 = Q \cdot L$$

$$V_1 = Q \cdot L \cdot R_2$$

$$L = D \left[h_{fe} \left(\frac{V_0 - 0.6}{R_1} \right) \right]$$

$$V_0 = \frac{D^{-1}[L] \cdot R_1}{h_{fe}} + 0.6$$

$$\frac{D^{-1}[L] \cdot R_1}{h_{fe}} + 0.6 = (V_2 - Q \cdot L \cdot R_2) \cdot A$$

$$V_2 - Q \cdot L \cdot R_2 = \frac{\frac{D^{-1}[L] \cdot R_1}{h_{fe}} + 0.6}{A}$$

Light is a function (unknown) of current.

As $\lim_{A \rightarrow \infty}$ this becomes

$$V_2 - Q - L - R_2 = 0$$

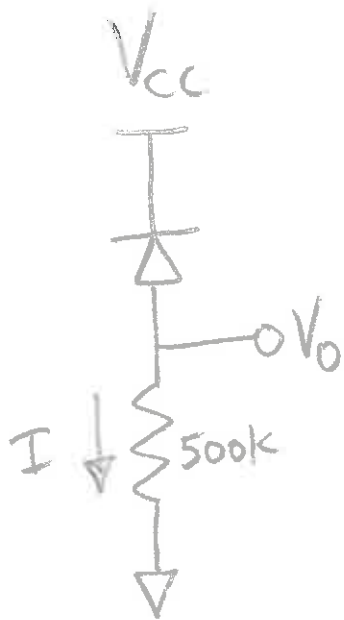
$$L = \frac{V_2}{Q - R_2}$$

$$L = \frac{1}{Q \cdot R_2} \cdot V_2$$

$$L = k \cdot V_2$$

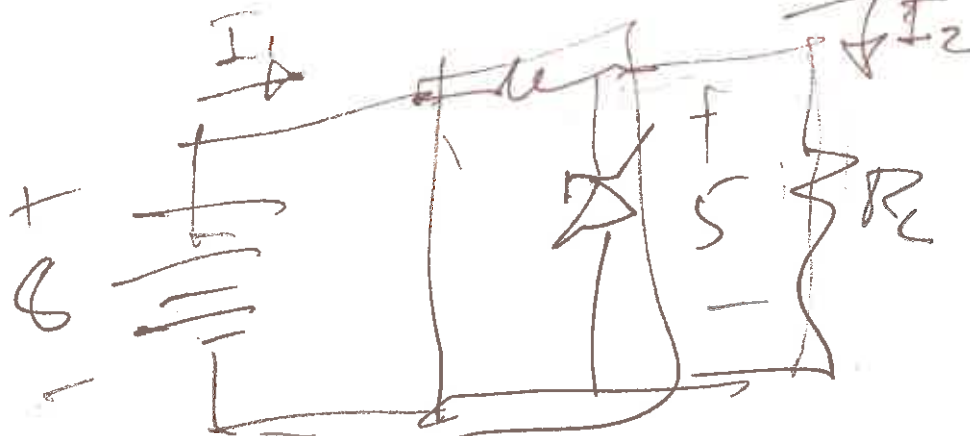
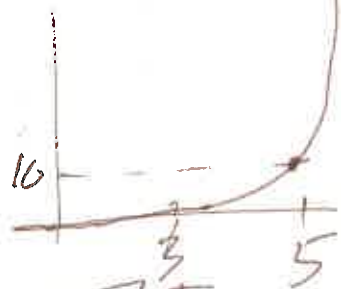
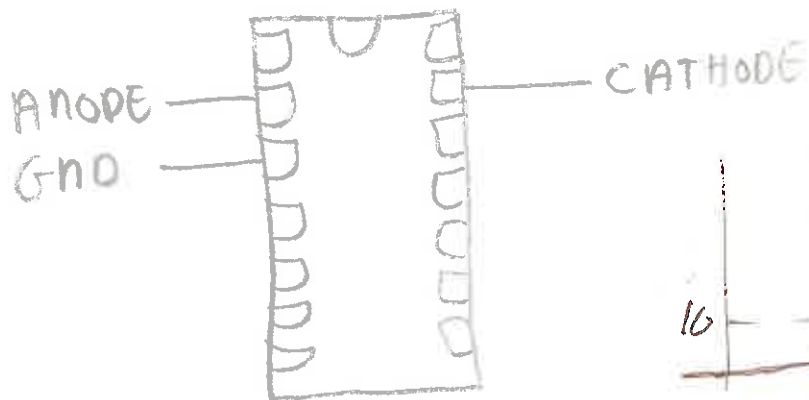
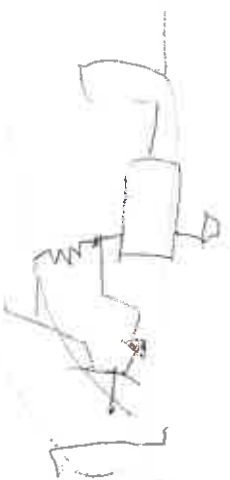
If efficiency of detector is constant over a large range, then light output will be proportional to V_2 .

DETECTOR II



V_{cc}	V_0	I (mA)
4	4.37	8.74
5	5.38	10.76
6	6.36	12.72
7	7.35	14.70
8	8.34	16.68

light power $\approx 10 \mu\text{Watts}$



INTO FIBER 100 mW
 OUT FIBER 7 mW
 LOSSES MEASURED 13 dB
 CALCULATED 11.6 dB (mech. splices)

EFFICIENCY $\frac{I}{P} = 0.6$

Peak current $\approx 4.2 \mu A$

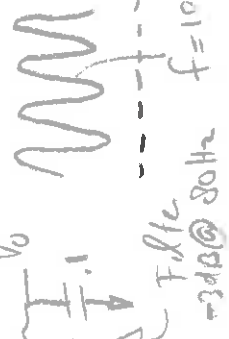
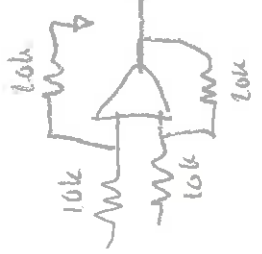
~~$2 \cdot \phi_{max} \cdot \sin 90 = 1.8$~~

$$P_{out} = \frac{1}{2} P_0 \left[J_1(2 \phi_m \sin \pi f \tau) \sin(\epsilon) \cos(\pi f t) \right] \sin \pi f (t + \tau/2)$$

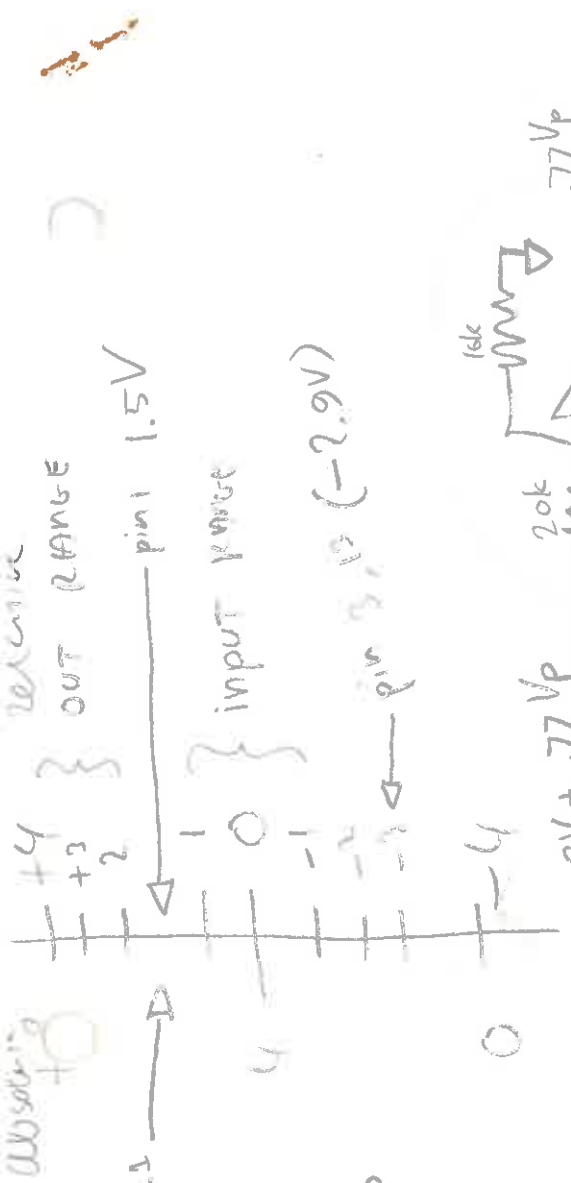
Multplier

Detailed (works)

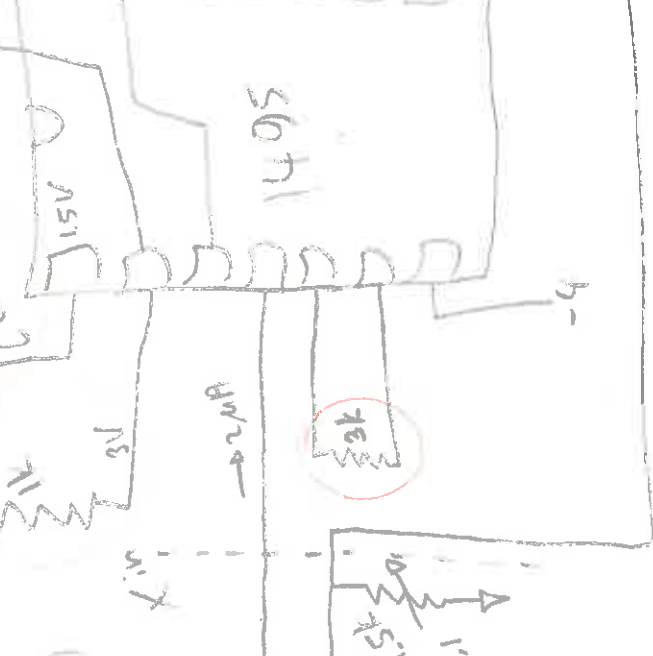
$V_{in} = 2 @ 1V_{p-p} \Delta f = 10Hz$
 $V_o = .1V_{p-p} @ .005 V_{off}$



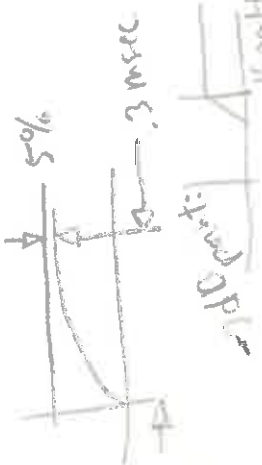
Results



From CONDUCTANCE
 $V_{offset} = 2mV$
 $X_c = 410\Omega$
 $R = 11\Omega$
 $\Delta = 5\%$



.6% of 50msec
 12 waves of 40kHz (2000 cycles)

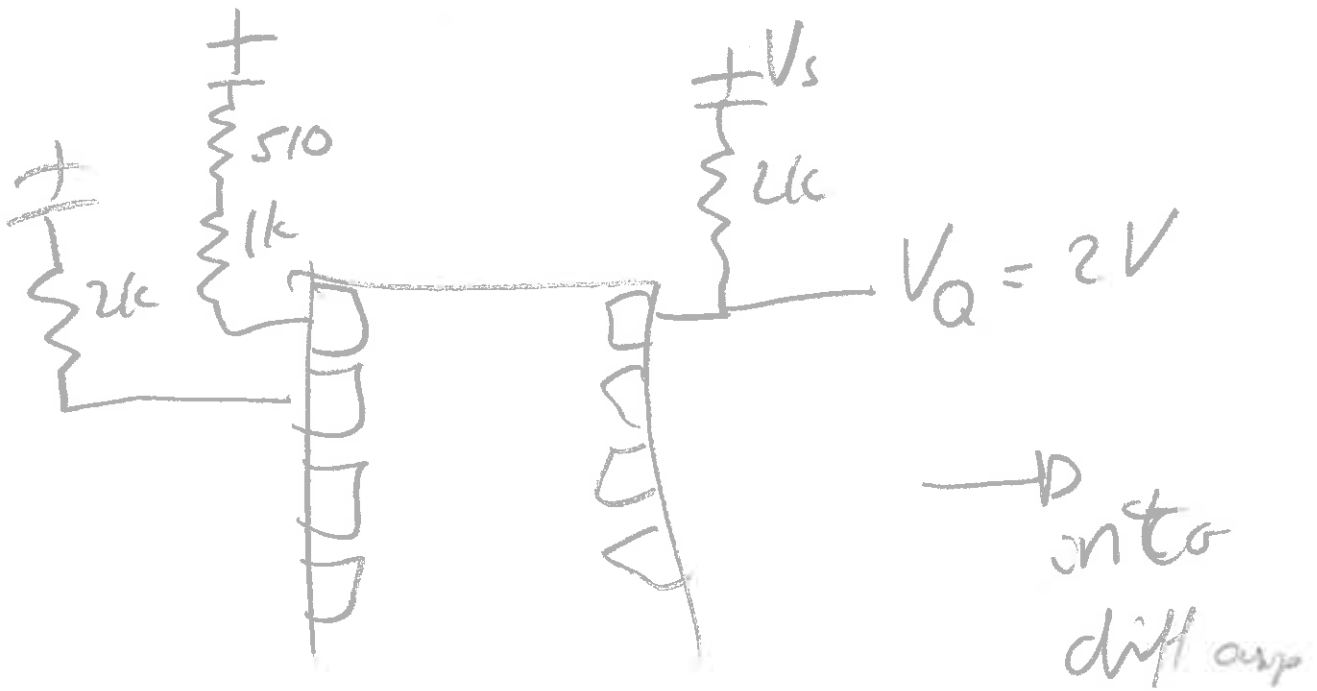


R_2 and R_3 have to be thermal
 bridge tracking

From OSCILLATOR



MULT II



for ~~old~~ voltage stability

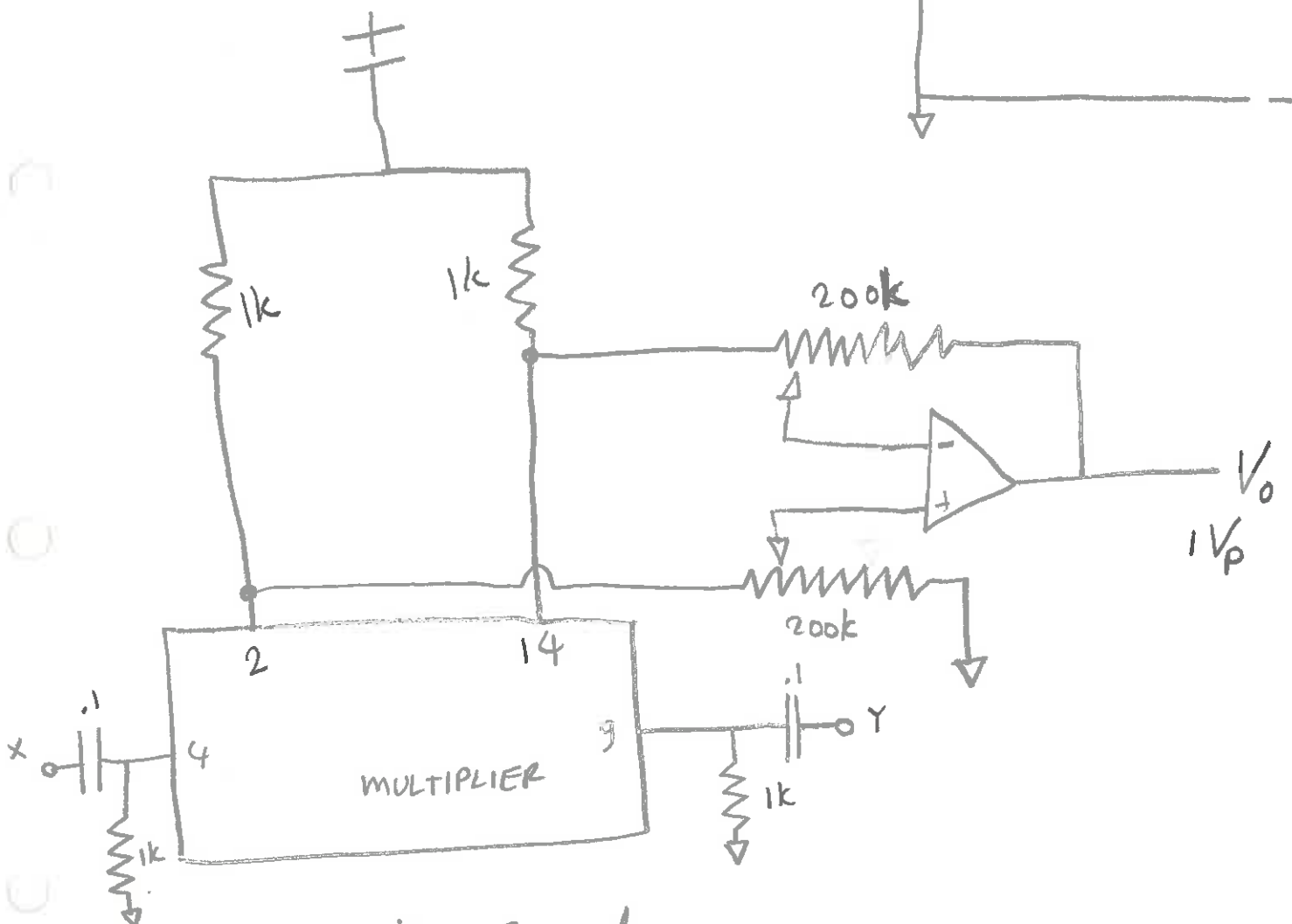
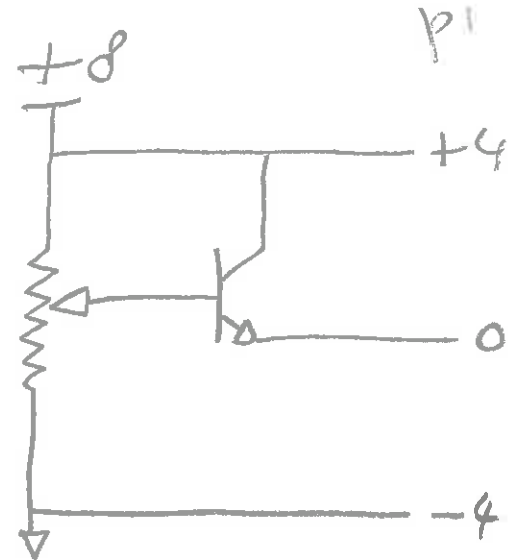
V_Q	V_{drift}
2	0.1

V_{supply} 3-4V
2.3V start of drift

10-36

Diff Amp

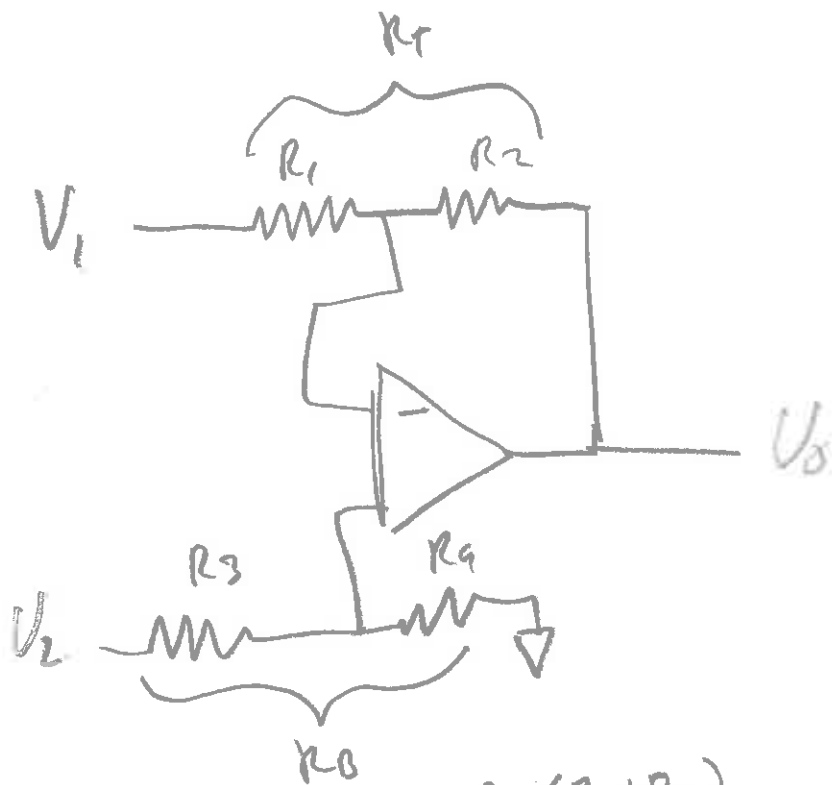
Bipolar Supply



Adjustment procedure

- I ground inputs
- II adjust one pot to zero output
- III ~~move both pots together (keep offset in output at zero) to change gain.~~

Diff Amp cont'



$$V_0 = V_2 \frac{R_4 (R_1 + R_2)}{R_1 (R_3 + R_4)} - V_1 \frac{R_2}{R_1}$$

$$= \frac{1}{R_1} \left[V_2 \cdot \frac{R_T}{R_B} \cdot R_4 - V_1 \cdot R_2 \right]$$

$$= \frac{1}{R_1} \left[V_2 \cdot x \cdot R_4 - V_1 \cdot R_2 \right]$$

$$\frac{R_T}{R_B} = x$$

$$R_2 = x \cdot R_4 \text{ when balanced}$$

with R_2, R_1 set:
adjust R_4 until $V_0 = 0$